OIPE JC68 JUN 14 2004 PATENT & TRADEMARK OFFICE

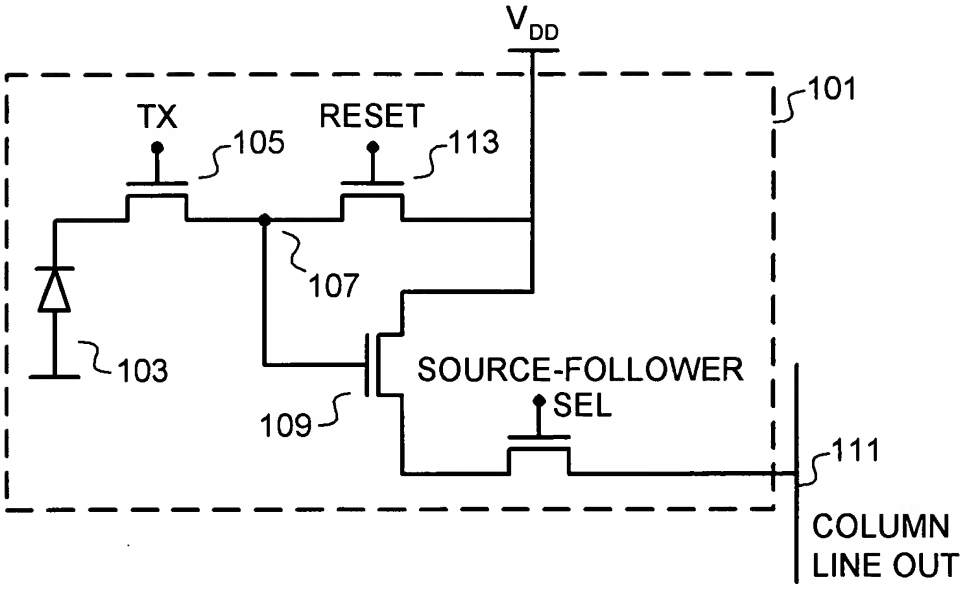

FIGURE 1
(PRIOR ART)

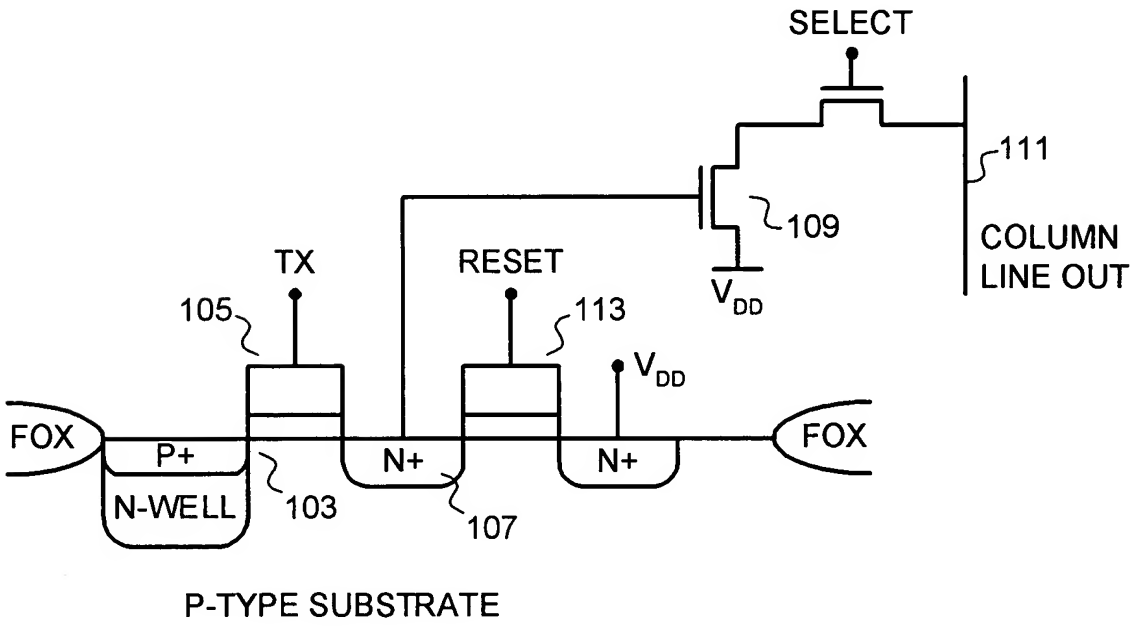

FIGURE 2
(PRIOR ART)

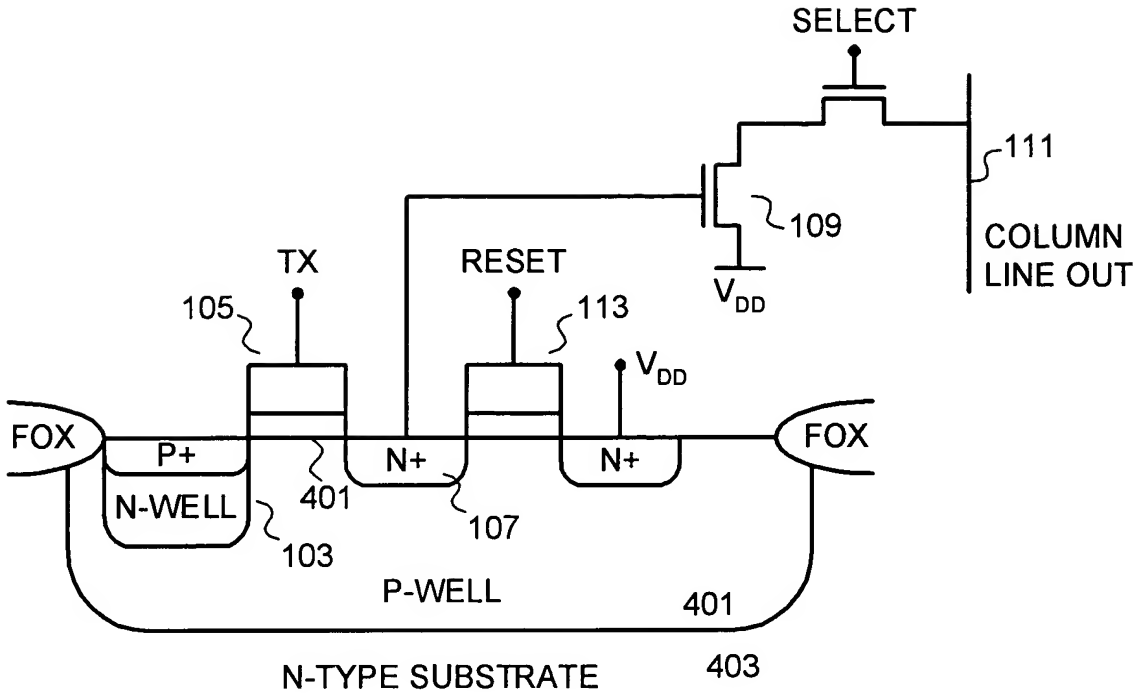

FIGURE 3

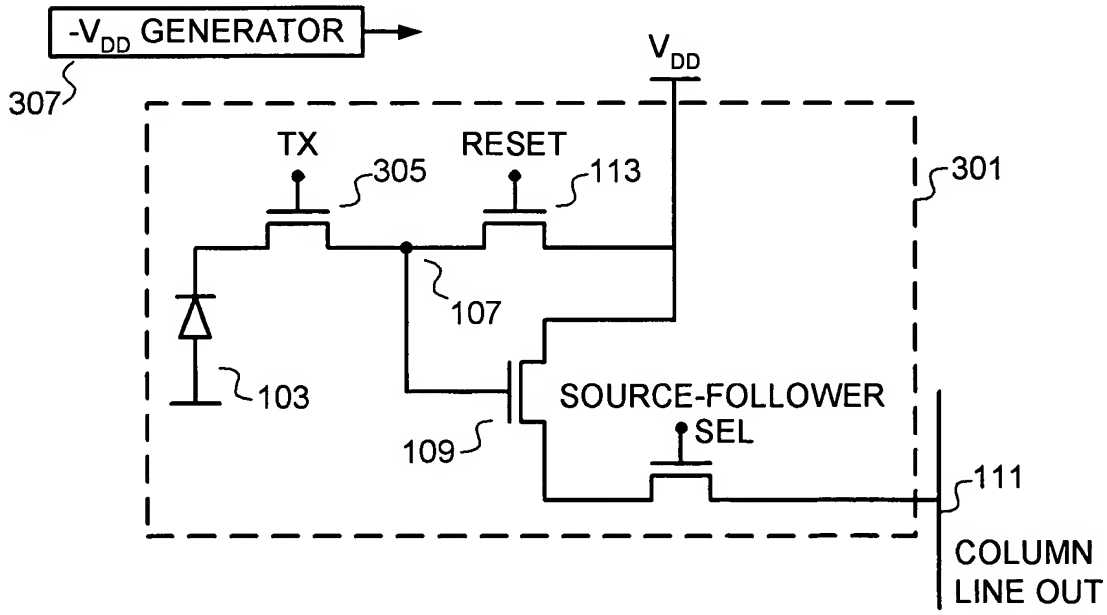

FIGURE 4

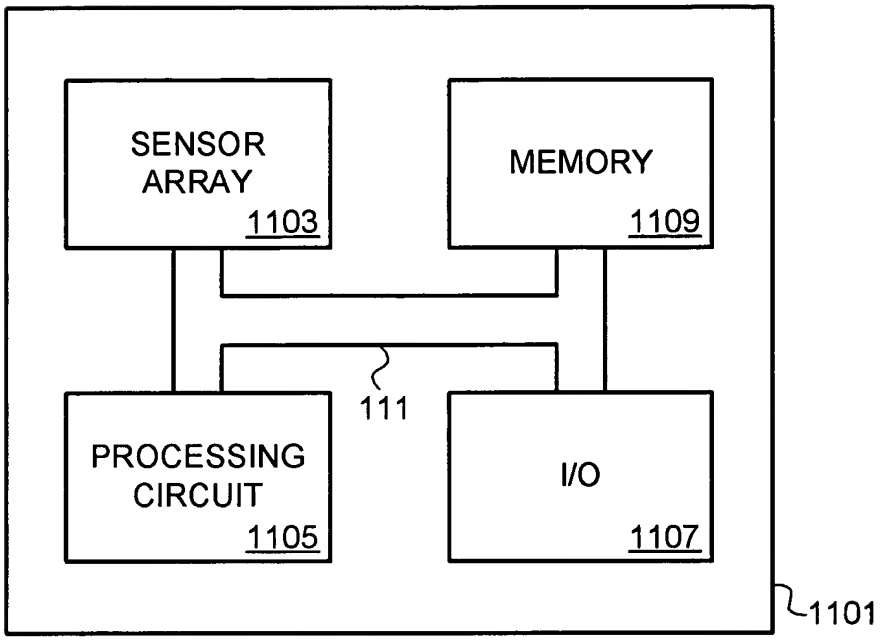
SENSOR ARRAY
1103
MEMORY
1109
111
PROCESSING CIRCUIT
1105
I/O
1107
1101

FIGURE 5